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**INFORMATION DISCLOSURE  
CITATION**

Docket No.: 0437-A-262  
Serial No.:  
Applicant: Ivanov et al.  
Filing Date: Herewith

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class/ Subclass
Jed	3,796,943	03/12/74	Nelson et al.	323/9
	4,321,648	03/23/82	Leonard Kaplan	361/101
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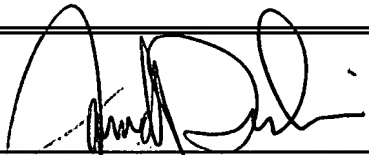
**OTHER DOCUMENTS**  
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"Dynamic Safe-Area Protection for Power Transistors Employs Peak-Temperature Limiting" by Robert J. Widlar and Mineo Yamatake, IEEE Journal of Solid-State Circuits, Vol. SC-22, No. 1, February 1987, pp. 77-84

"A 15-W Monolithic Power Operational Amplifier" by Paul R. Gray, IEEE Journal of Solid-State Circuits, Vol. SC-7, No. 6, December 1972, pp. 474-480

"High-Gain 15-W Monolithic Power Amplifier With Internal Fault Protection" by Ernest L. Long and Thomas M. Frederiksen, IEEE Journal of Solid-State Circuits, Vol. SC-6, No. 1, February 1971, pp. 35-44

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Examiner		Date Considered	6/17/03
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